Search

Search

Recent Posts

Recent Comments

No comments to show.

PHY335 – Electronics and Instrumentation Lab –

Spring 2025 **HOME**

PHY 335: Electronics and Instrumentation Laboratory –

Spring 2025 **Organization**

Room A-127 + TBD

L01: Tue/Thu 12:30PM-3:20PM & L02: Mon/Wed 2:00PM-4:50PM

Classes: Classroom:

Office Hours:	Mon 1:00PM-2:00PM (Corliss),
	TBD
	TBD
Professor:	Ross Corliss
	Office: C-103
	Office hours: See above. Online via email, or zoom by appointment
	Email: ross.corliss (at) stonybrook.edu
Teaching Assistants:	TBD (L01: Tue/Thu)
	TBD(L02: Mon/Wed)
Prerequisite:	PHY 251 and WRT 102
Credits:	3
SBC:	TECH
Description	
Students will desigr	n, build, and test basic DC and AC circuits which perform a useful function, a
viewed by physicist	s, involving resistors, capacitors, transformers, diodes, transistors, and oper

ciated fee. Please see www.stonybrook.edu/coursefees for more information.

material covering the listed topics. In groups of 2 (rarely 3), you will perform lab assignments. Time in the lab is limited, and extensions will be only be granted in exceptional circumstances. You should make a best effort to participate equally in the experimental work, including making sure you are giving your labmate the opportunity to participate. You will write separate lab reports after each unit, and submit them for grading along with your lab book. You may work on these reports with your labmate, but you are responsible for all contents of your submission: You should be able to explain all parts of it without your partner's support. Any attempt to copy

ational amplifiers. Students will measure these circuits using digital multi-meters and digital os-

plied to operational amplifiers. Two three-hour laboratories per week. This course has an asso-

The course is arranged in units which cover related topics. Each unit will span multiple lab peri-

ods. For preparation at the start of each unit, read the unit instructions (linked below) and the

cilloscopes. Understanding of analog circuits will be stressed including negative feedback ap-

grade and possible further action. Most lab periods will start with a short lecture. Please be on time. You should make a best effort to participate equally in the experimental work, including making sure you are giving your partner the opportunity to participate. You will write separate lab reports after completion of each unit and submit them for grading along with your lab book. You

may work on the report with your partner, but we require that both of you have ownership of

the report — you need to be able to explain all parts of it without your lab partner's support.

Any attempt to copy from other people's reports or to make up data is academic misconduct

from other people's reports or to make up data is academic misconduct and will lead to a zero

and will lead to a zero grade and possible further action. **Laboratory Etiquette** You will be working in a lab space shared with other students. Please keep the lab clean and return components to the correct parts containers when finished with them. If your workbench is found untidy after class, points may be deducted. If you throw away working parts to clean up faster, points **will** be deducted.

Lab reports comprise 85% of each unit's grade. They should be prepared on a computer, e.g.

An example LaTeX document, including the Unit 0 report is available on overleaf here

with LaTeX or Word. I highly recommend LaTeX, for example using overleaf.com (which is free).

If supplying a typeset report is a hardship, please come speak with me so we can find a solution. The reports should include:

Introduction (30 points)

• 1 to 2 pages

form

port)

• Draw circuit diagrams!

Lab Reports

the lab instructions) • Data (20 points) • Describe the circuit you built, the raw measurements you took, and the procedures. • Present the data (generally, all data in the lab notebook should be also in the lab re-

• Describe the electronic components you are studying, and the studies you will per-

• Include all relevant theory and equations (generally those found in bold at the top of

 Analysis (30 points) • Describe the calculations that convert the raw measurements into the derived quantities that connect to the theory in your introduction.

• Include a discussion of statistical and systematic errors

• Include error bars on plots and in data tables

 Does experiment agree with theory prediction? • Explain if the experiment was successful. If not, propose what one could do next (ie a way to correct a problem that was encountered).

• Short conclusion / summary (5 points)

The Introduction and conclusion must be your own work. If you collaborate with others on other sections, you may turn in identical texts, provided each copy notes clearly the names of all collaborators. Note that you are responsible for the material. You need to be able to explain

may be deducted for excessive blather in your reports.

before class begins. The game plan should include:

and the **calculations/equations** used to derive those.

data points you intend to take across that range.

the course of each session. The contents must be complete:

each part of it alone without your lab partners support. **Beware Blather:**

The text you write should remain specific and pertinent to the topic. Long asides about possi-

no penalty for proposing, in good faith, a source of error that is not actually an issue, but points

• Summarize the measurements you made and their relation to the theory.

- ble sources of error that do not plausibly come into play, vague assertions of the need for proper techniques and equipment, etc, contribute to unfocused, boilerplate-like text that I call "blather". Be aware that LLMs like ChatGPT are particularly prone to this sort of text. There is
- notebook. It must be of a type where it is not easy to add/remove pages. These books will contain all your notes and data. Scores are based on your **game plan** and your **completed notes** for each unit.

The **game plan** will be inspected during the first session of each unit, so you should prepare it

• The **equations** you expect to need in order to derive key values and to take and assess

• Listings of values for each circuit component you expect to use, in appropriate SI units,

• Layout of the data you intend to take, including what values you will record (eg 'Voltage

at point B', 'Frequency'), what range of input values you expect to go over, and how many

Lab notebooks comprise 15% of the grade for each unit. You must have a physical, paper lab

data • Circuit diagrams for all circuits you expect to build during the lab

• All circuits you build

are not a substitute.

or copy of that data as well.

Exams

ams.

Grading

Midterm exam

Final exam

Total

Game Plan

Lab Notebooks

The purpose is to show you have prepared for the unit before arriving. **Completed Notes**

Your notebook is a record of what you have done in lab, and you should add things to it during

You will not be penalized for incorrect values or diagrams that you have drawn in good faith.

• All measurements you make must be there. (If you are taking a very large dataset (more than 5-10 measurements) you may record at least five measurements in the notebook and note that the rest are recorded in a google sheet or other web-accessible form.) You are welcome/encouraged to augment these notes with phone pictures etc., but those

• All calculations you perform, sketches of waveforms, notes about what went wrong, etc.

Instead, cross out the error neatly, write what was wrong, and then write the correct version.

After finishing a unit you will turn in your lab notebook to your TA for grading. This can be ei-

ther a single pdf containing legible photos of each page in your lab notebook, or else your

physical lab notebook itself (in which case you should have a second lab notebook to use

for the next unit). If you have additional data in digital format, you must provide a link

• You are may write in pen or pencil. If you make a mistake, **do not erase or scribble out**.

There will be **midterm exam** during the semester, and a **final exam** at the end of the semester. Exams include a practical part, where you will have to complete experimental tasks in the lab, and a written part, where you will have to explain the relevant theory, design circuits, and analyze or otherwise derive results for given circuits. Take notes at mini-lectures to prepare for this.

Each exam will resemble the lab period and the writing of the report, all combined in the inter-

val of 1/2 a lab period. The exams are given in two shifts, so that each student will have to work

on the exam problems on his or her own. Active and equal participation in experimental work

The grade for each of the units will be based on lab books (15 points) and lab reports (85

points). At least six units (unit 0 and unit 1 count as one unit total), the midterm, and the final

and study of the material covered in mini-lectures during the course will prepare you for the ex-

must be completed to pass this course. Your base grade will be based on the six highest unit grades, while the seventh will be converted to bonus points (at a discounted rate). Assessment/Assignment/Exam **Points or Percentage** Units, including lab book and lab report $10 \times 6 = 60$

The grading is weighted as **60% Units + 20% midterm + 20% final**. The grading scale is A 95-

These thresholds are a general guideline, and may be modified depending on class perfor-

100; A- 90-94; B+ 86-89; B 83-85; B- 79-82; C+ 75-78; C 71-74; C- 67-70; D+ 64-66; D 60-63; F 0-59.

maining, the unit will be graded as normal, but will be capped at a B- (80). There will be chances

If you have an exceptional situation and will be late beyond this, come talk to me. Note, howev-

er, that a very busy week is not an exceptional situation. Budget your grace days accordingly.

to earn additional grace days throughout the semester for those that need them.

20

20

100

Late Work Policy: Reports are due to the TAs on the calendar day listed below. You start with a total of **14 grace days**. Late submissions will be accepted at no penalty, but for every calendar day late a submission is, I will deduct a grace day. If you do not have enough grace days re-

Textbooks

tents/ to find the comparable chapters.

Other textbooks include:

Course Schedule

Topic

Introduction

Simulation and

Operational Ampli- 3/05,3/06

PCB Design

fiers

Midterms Exam on units 1-5

Transistors

topics.

Unit

0

4

5

6

mance.

cover all details of the material. I recommend **Horowitz and Hill**, **The Art of Electronics** (Cambridge University Press, 2nd or 3rd edition). This book is an excellent, concise resource, and has a great deal of material beyond what will be covered in this course. Rizzoni, <u>Principles and Applications of Electrical Engi-</u> neering (McGraw-Hill), often has more examples and more mathematical detail. You should

find a textbook style that suits you. Suggested readings are given as AoE 3rd edition chapters.

If you are using a different text, you can use https://artofelectronics.net/the-book/table-of-con-

• Curtis A. Meyer, <u>Basic Electronics: An Introduction to Electronics for Science Students</u>

Find a book with a style that suits you. The mini-lectures are not enough to cover the required

Note that the syllabus and dates might change. Readings are based on Art of Electronics 3rd

edition. If you are using a different text, you can use https://artofelectronics.net/the-book/ta-

Gameplan/ Report

Due

2/12,2/13

3/10,3/11

4/07,4/08

4/23,4/24

Notebook

Check

buy one.

2/26,2/27

3/05,3/06

4/02,4/03

Reading and Addition-

Unit 0 report template

Download and try out

LTSpice, KiCAD. For the

notebook check, have

them installed and

downloaded!) lecture

AoE ch 4, lecture (pdf)

Review will take place

AoE ch 2 and 3, lecture

on 3/26,3/27

running (not just

(pdf)

(LaTeX / overleaf)

(google doc)

al Material

Hayes and Horowitz, <u>Student Manual for the Art of Electronics</u>

Dates (MW,TuTh)

1/27,1/28

2/26,2/27,

3/03,3/04

3/10,3/11,**3/12**,3/13,

3/24,3/25,**3/26**,3/27

3/31, 4/01

4/02,4/03,

Alexander and Sadiku, <u>Fundamentals of Electric Circuits</u>

R. Cogdell, <u>Foundations of Electrical Engineering</u>

ble-of-contents/ to find the comparable chapters.

There is no specific required textbook, but note that reading a textbook section covering the

topic of each lab section is **extremely highly recommended**, since in-class lectures will not

Lab Instruments, **1/29**,1/30, **2/17**, 2/18 AoE ch 1.1-1.3, lecture **2/03**, 2/04 Signals, Resistors **2/03**,2/04,**2/05**,2/06 (pdf) 2 Capacitors, Induc-**2/10**,2/11,**2/12**,2/13, **2/10**, 2/11 **2/24**,2/25 AoE ch 1.4,1.5,1.7, lectors, RC Filters **2/17**,2/18 ture (pdf) 3 **2/19**,2/20 AoE ch 1.6, lecture (pdf) Diodes and DC **2/19**,2/20, **3/03**,3/04 **2/24**,2/25 power

4/07,4/08,4/09,4/10, (pdf) **4/14**,4/15,**4/16**,4/17 7 **4/21**,4/22,**4/23**,4/24, Digital Electronics AoE ch 10,(11),12.1-**4/21**,4/22 5/09. 12.3,13.1-13.5, (13.5-**4/28**,4/29,**4/30**,5/01, **5/05**,5/06 13.14), lecture (pdf) Review will take place Finals Exam on units 1-7, **5/07**, 5/08 on **5/05**,5/06 focus on 6-7 **Additional Material** Professor Bernauer has made several helpful tutorials which may be of interest: Gnuplot tutorial 1: Video , data file , script file LTSpice tutorial: Video KICAD tutorial: Video Soldering tutorial: Video **Learning outcome** By the end of the semester, students will be able to apply technical tools and knowledge to practical systems and problem-solving: Describe the function and uses of basic electronic components Design and construct simple circuits for a variety of purposes Perform measurements with a DMM and oscilloscope • Analyze the performance of simple circuits Perform basic data analysis including error propagation **Student Accessibility Support Center Statement**

Academic Integrity Statement Each student must pursue his or her academic goals honestly and be personally accountable

for all submitted work. Representing another person's work as your own is always wrong. Facul-

ty is required to report any suspected instances of academic dishonesty to the Academic Judi-

ciary. Faculty in the Health Sciences Center (School of Health Professions, Nursing, Social Wel-

If you have a physical, psychological, medical, or learning disability that may impact your course

work, please contact the Student Accessibility Support Center, Stony Brook Union Suite 107,

(631) 632-6748, or at sasc@stonybrook.edu. They will determine with you what accommoda-

Students who require assistance during emergency evacuation are encouraged to discuss their

needs with their professors and the Student Accessibility Support Center. For procedures and

ty/emergency-evacuation/evacuation-guide-disabilities and search Fire Safety and Evacuation

information go to the following website: https://ehs.stonybrook.edu//programs/fire-safe-

and Disabilities.

Edit

Proudly powered by WordPress

tions are necessary and appropriate. All information and documentation is confidential.

fare, Dental Medicine) and School of Medicine are required to follow their school-specific procedures. For more comprehensive information on academic integrity, including categories of academic dishonesty please refer to the academic judiciary website at http://www.stonybrook.edu/commcms/academic_integrity/index.html **Critical Incident Management**

Stony Brook University expects students to respect the rights, privileges, and property of other people. Faculty are required to report to the Office of Student Conduct and Community Standards any disruptive behavior that interrupts their ability to teach, compromises the safety of the learning environment, or inhibits students' ability to learn. Faculty in the HSC Schools and the School of Medicine are required to follow their school-specific procedures. Further information about most academic matters can be found in the Undergraduate Bulletin, the Undergraduate Class Schedule, and the Faculty-Employee Handbook.